

FIG. 1

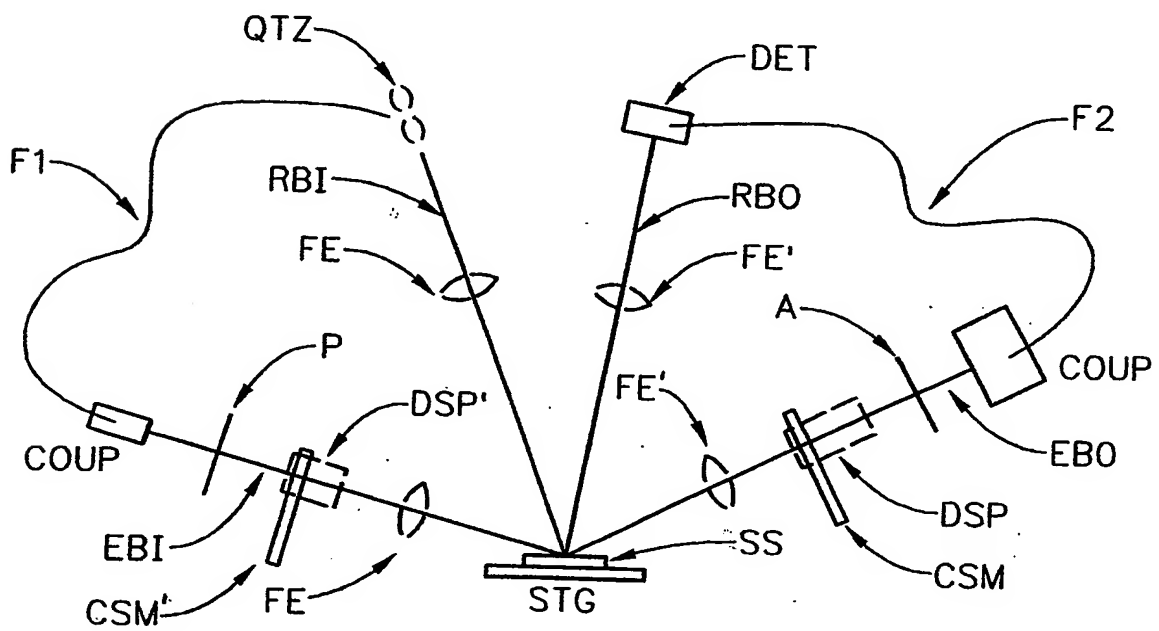
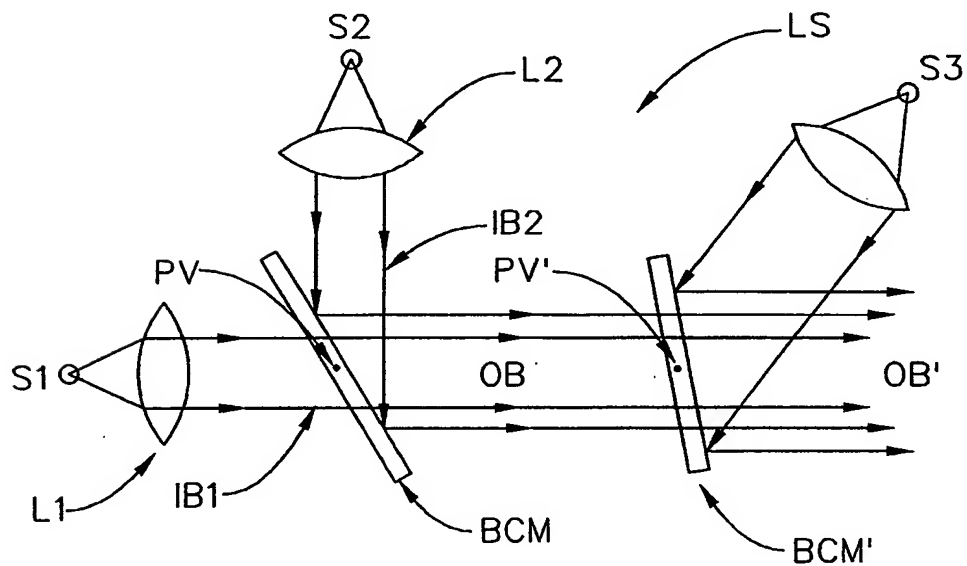
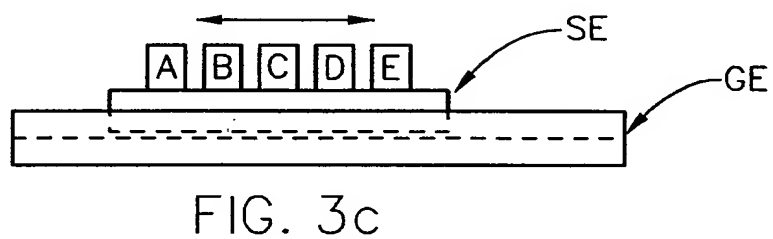
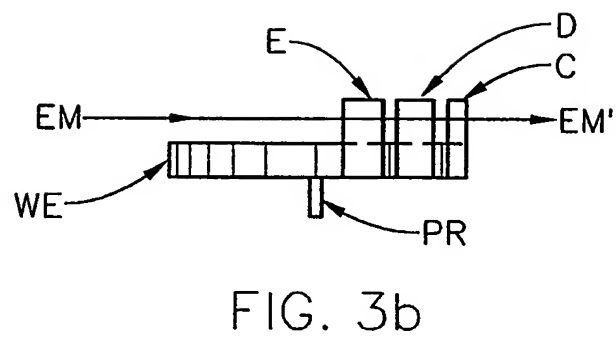
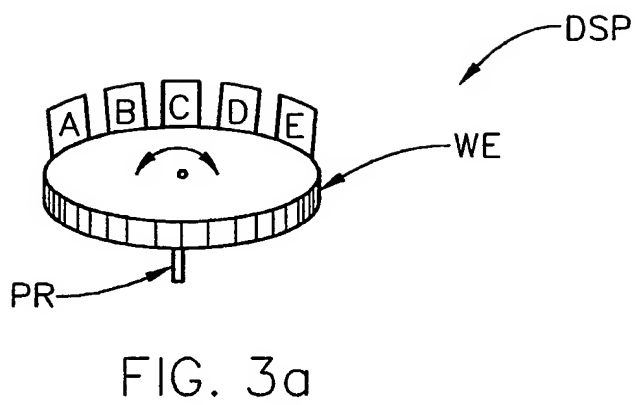


FIG. 2



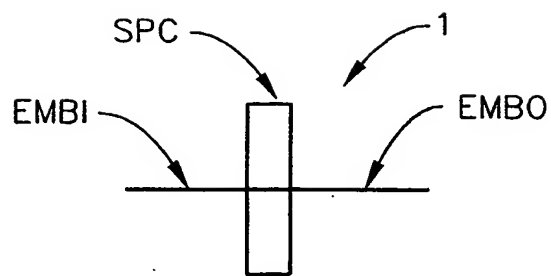


FIG. 3e

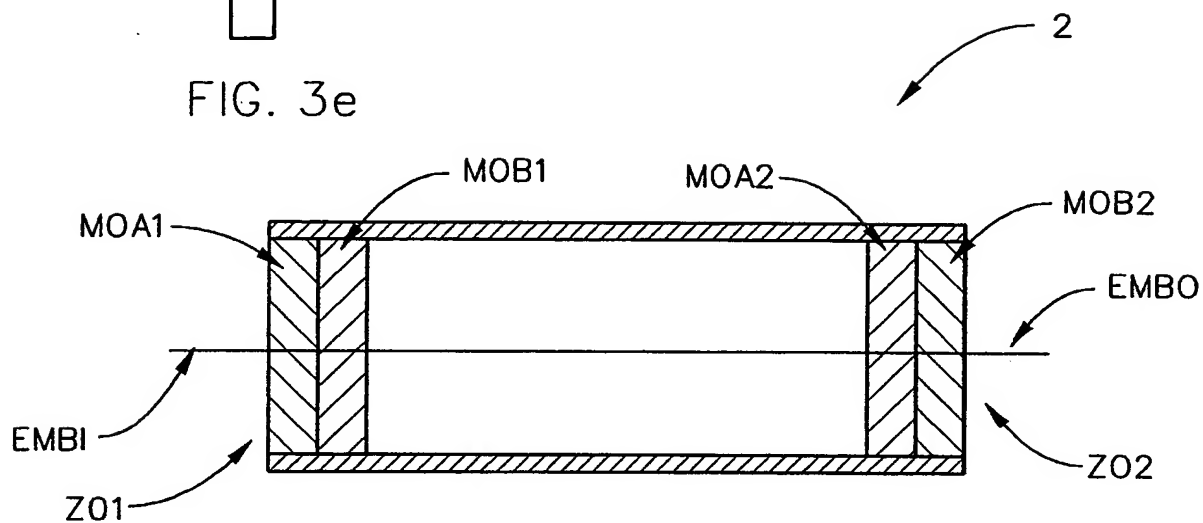


FIG. 3f

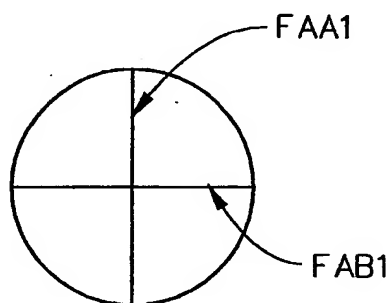


FIG. 3g

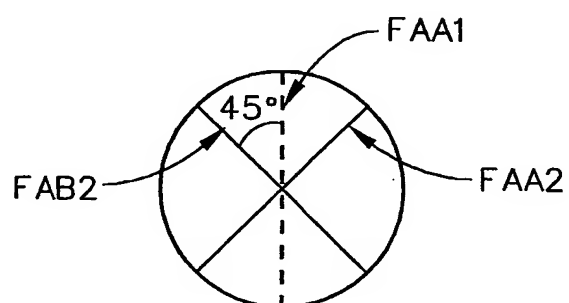


FIG. 3h

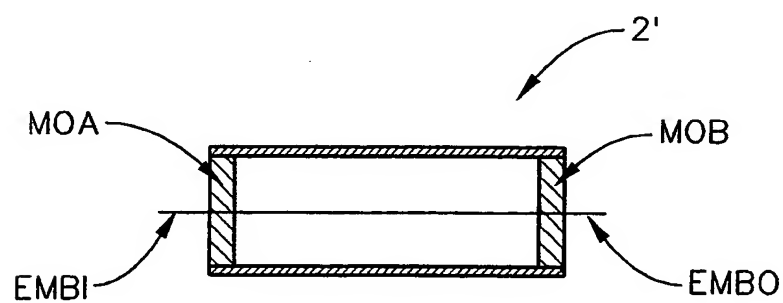


FIG. 3i

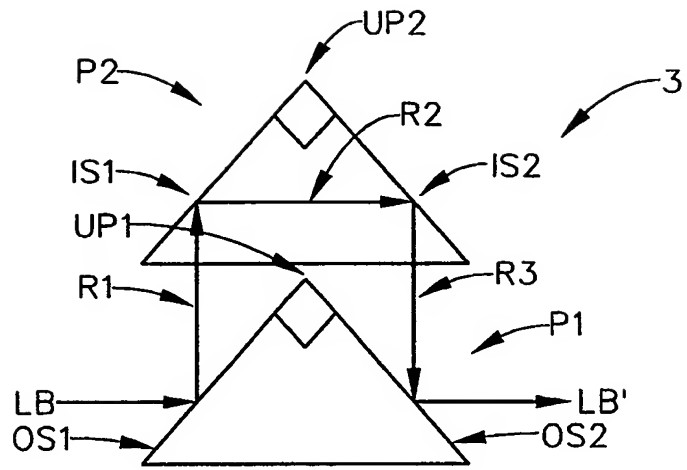


FIG. 3j<sub>1</sub>

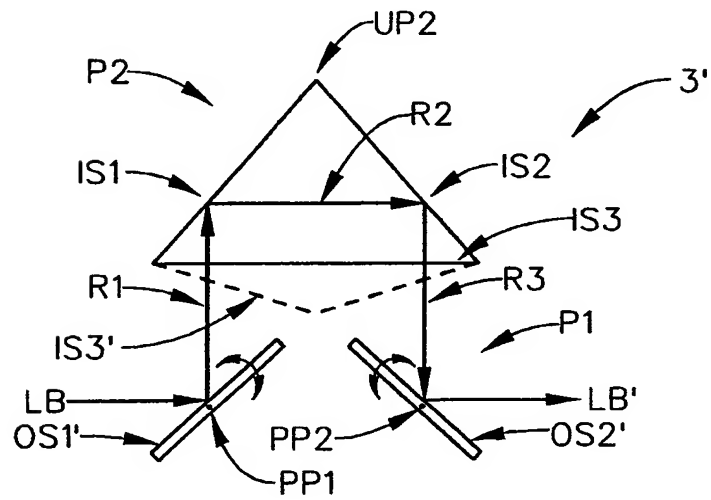


FIG. 3j<sub>2</sub>

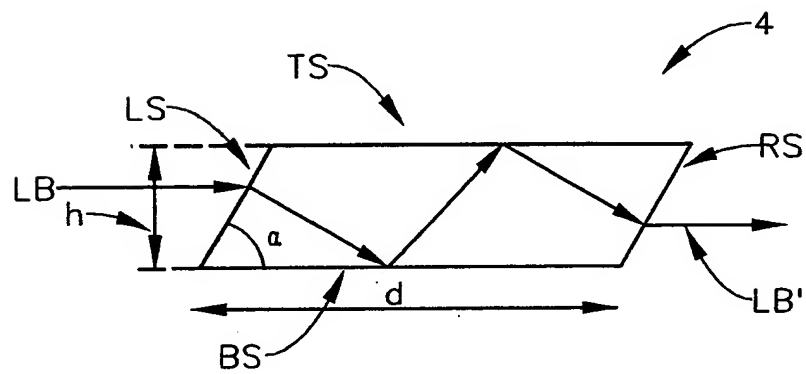


FIG. 3k

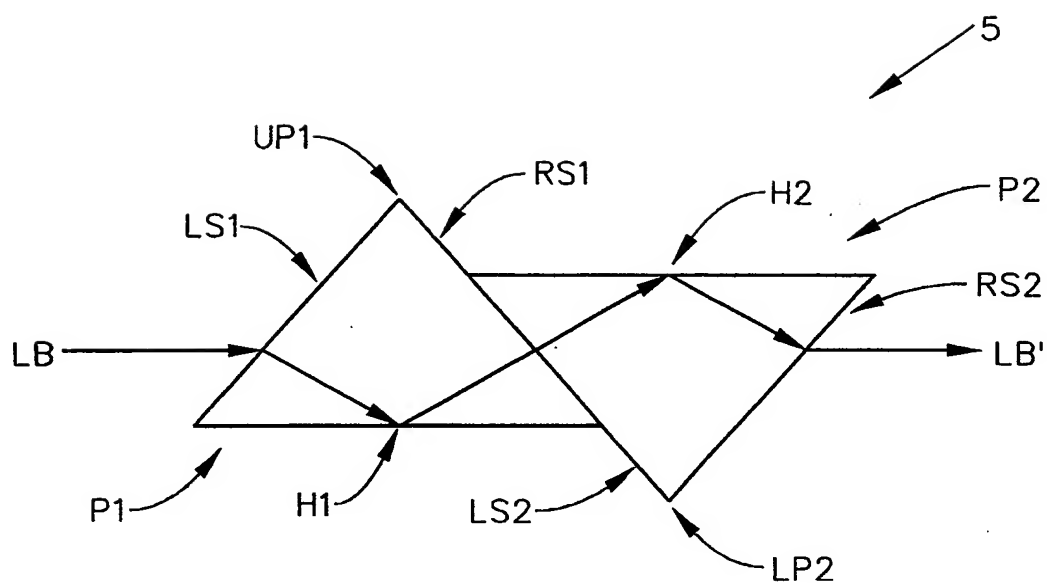


FIG. 3l

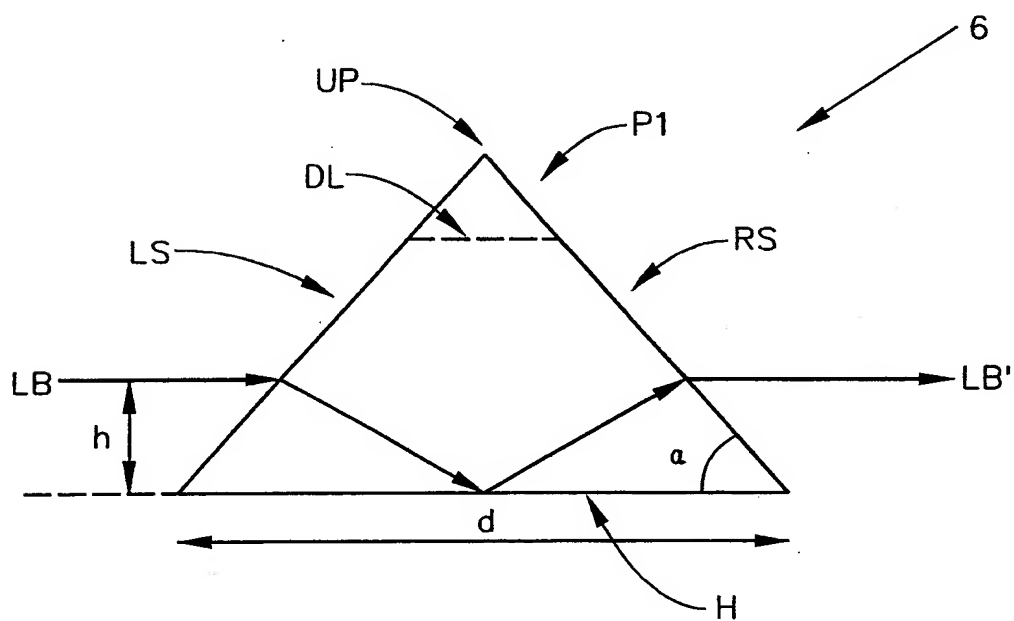


FIG. 3m

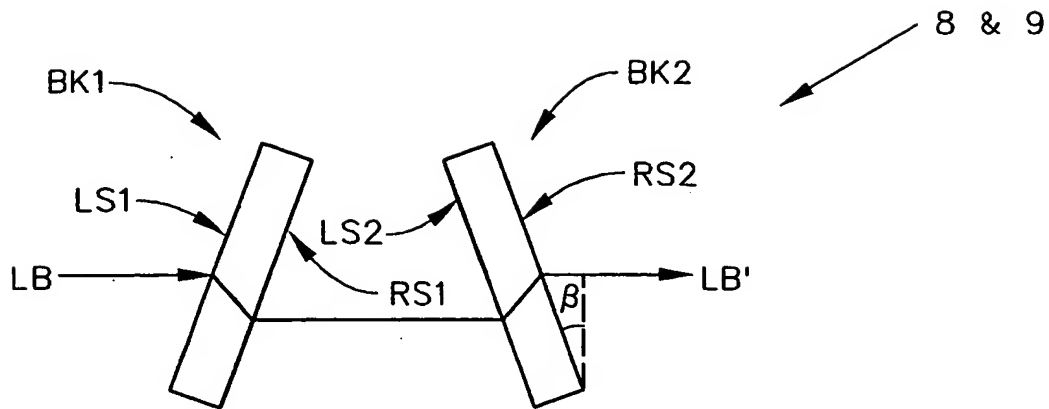


FIG. 3n<sub>1</sub>

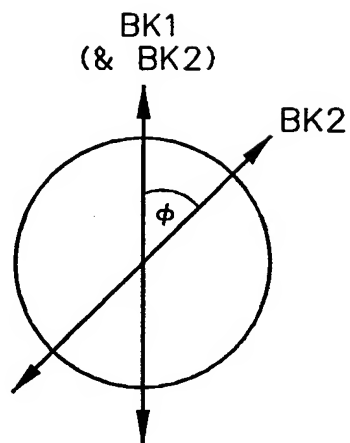


FIG. 3n<sub>2</sub>

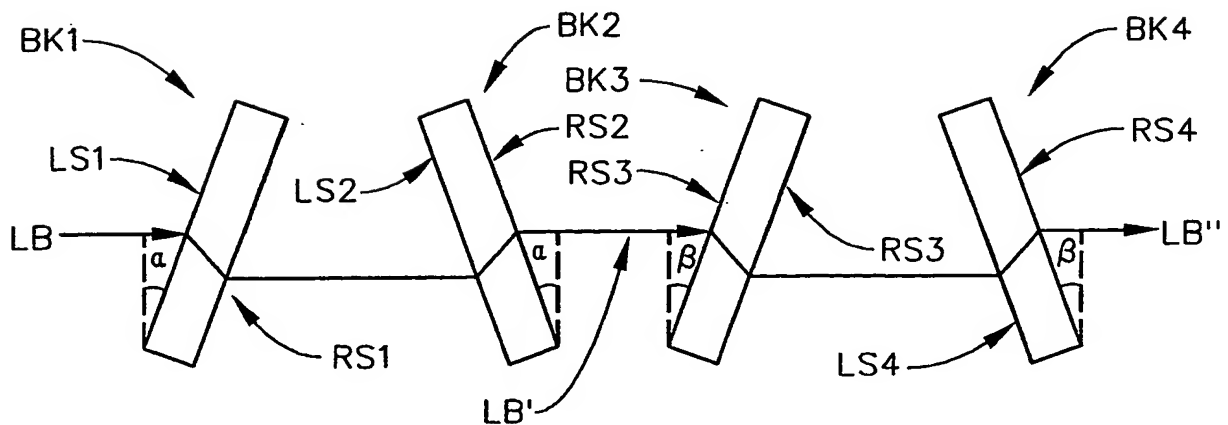


FIG. 3o<sub>1</sub>

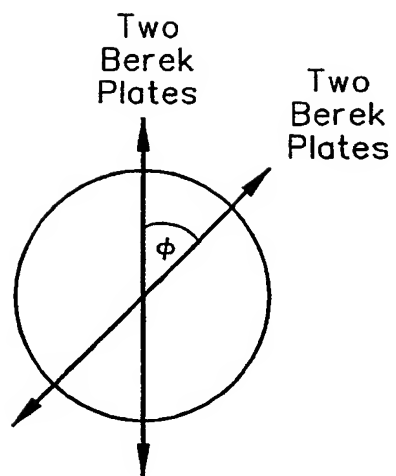


FIG. 3o<sub>2</sub>

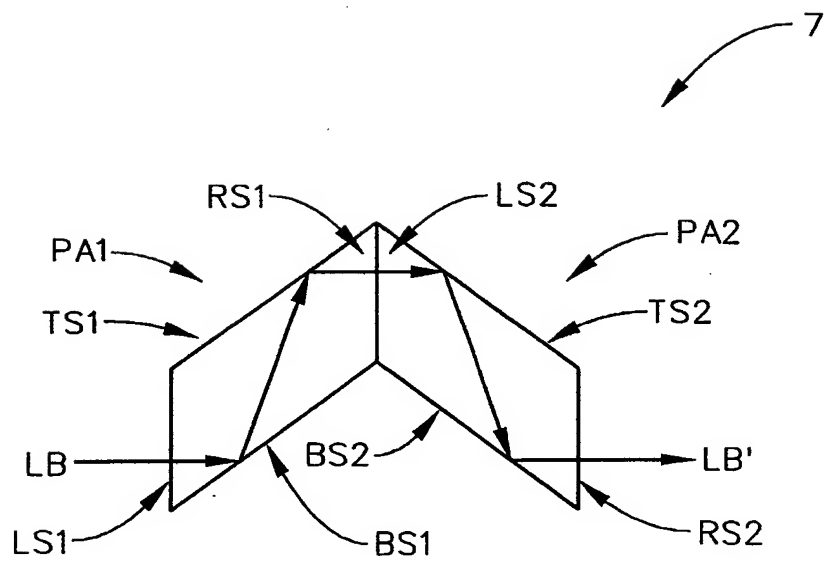


FIG. 3p

COMPARISON OF SINGLE vs. DUAL  
WAVEPLATE COMPENSATOR DESIGN

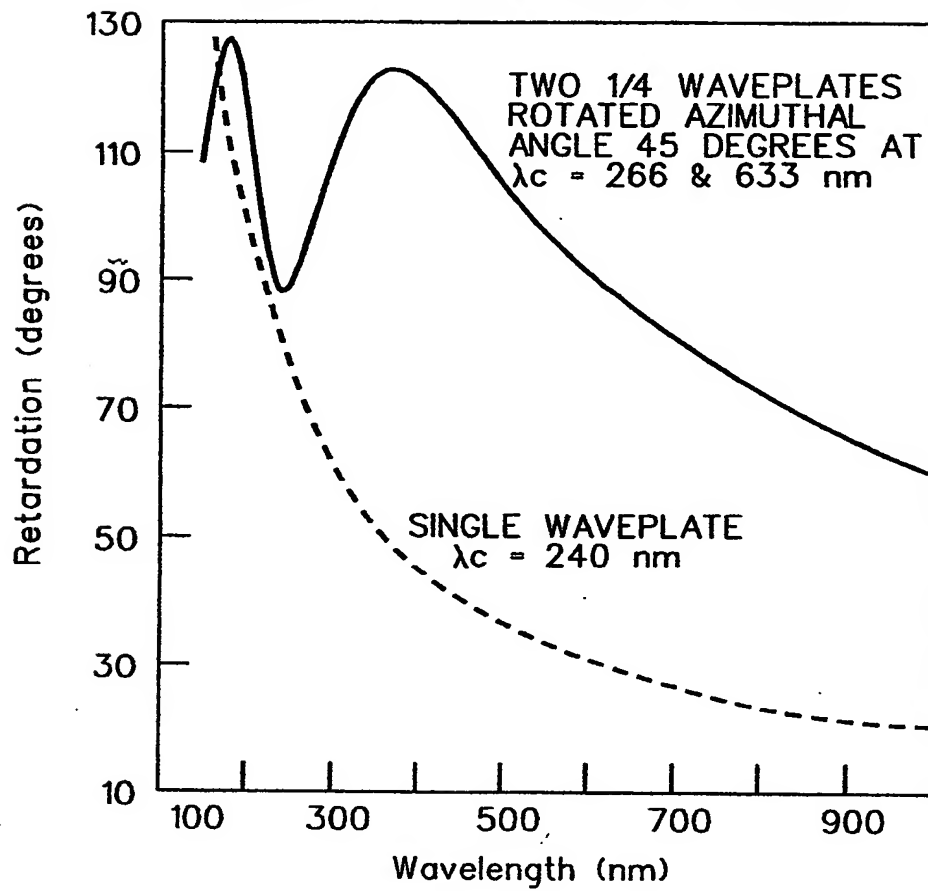


FIG. 4



Retardance Characteristics of Waveplates  
used in DualElement Compensator Design

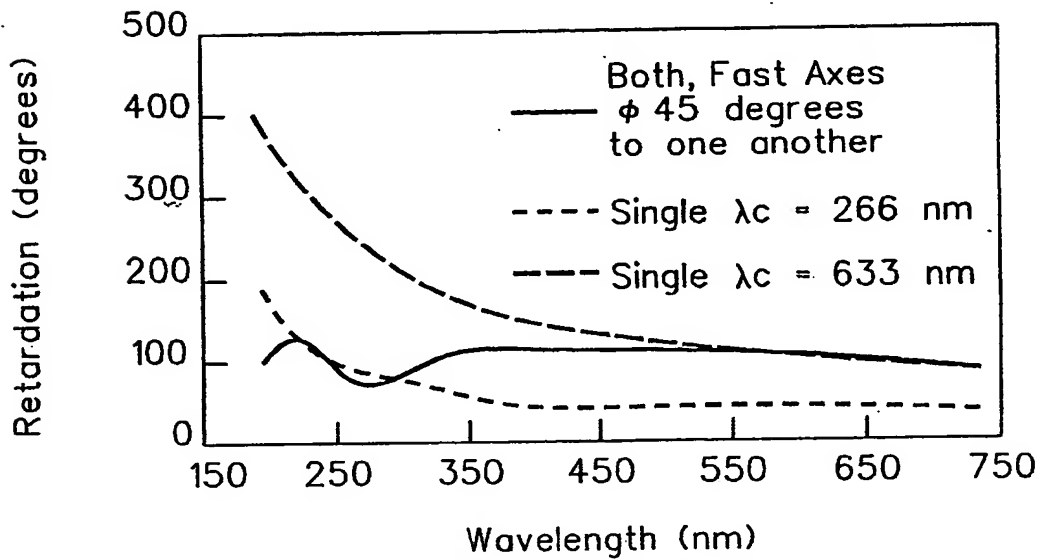


FIG. 5

Present Invention DualElement Design  
for  $\lambda_c = 266, 633$  nm &  $\phi = 45$  degrees

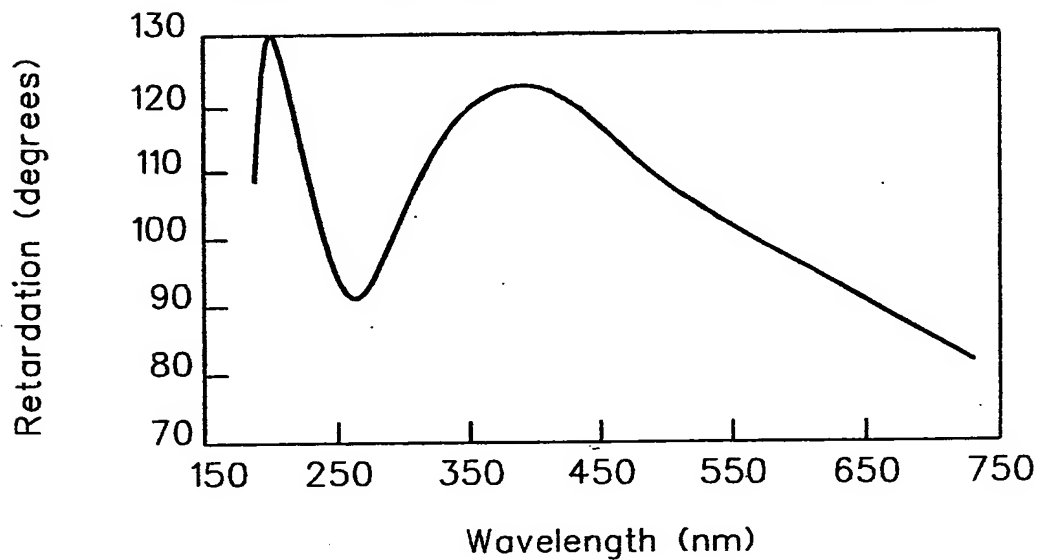


FIG. 6

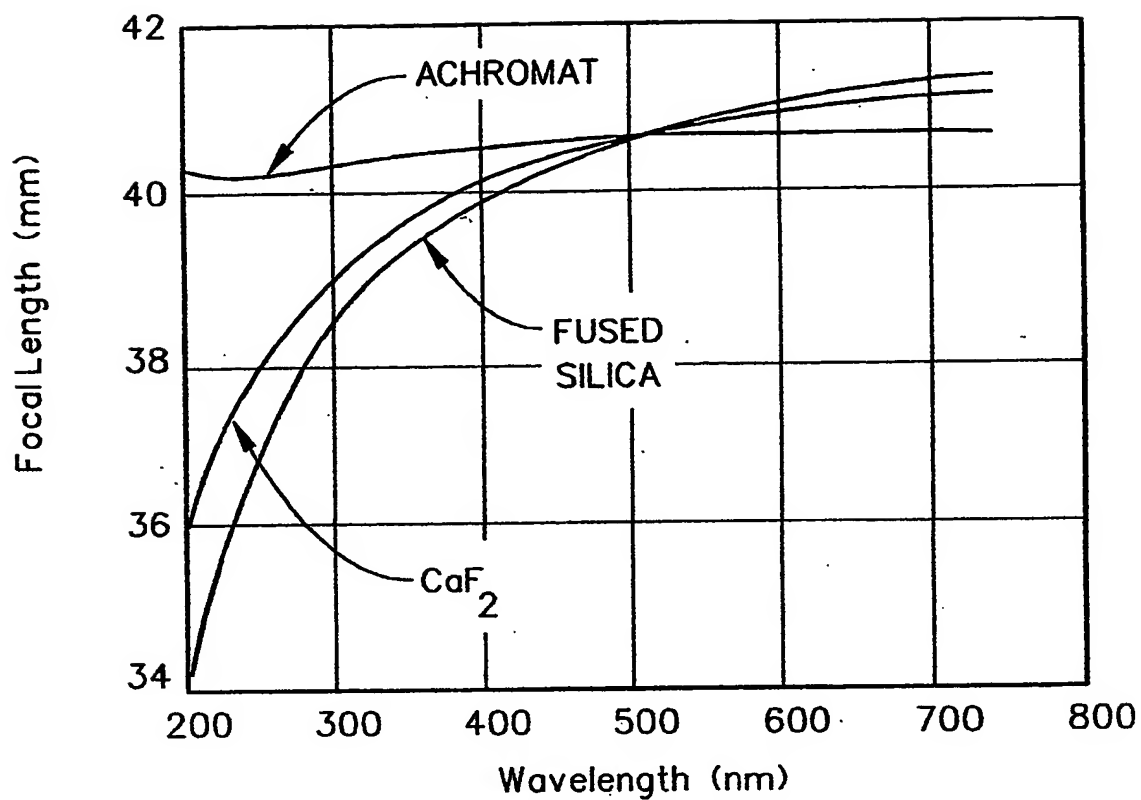


FIG. 7a1

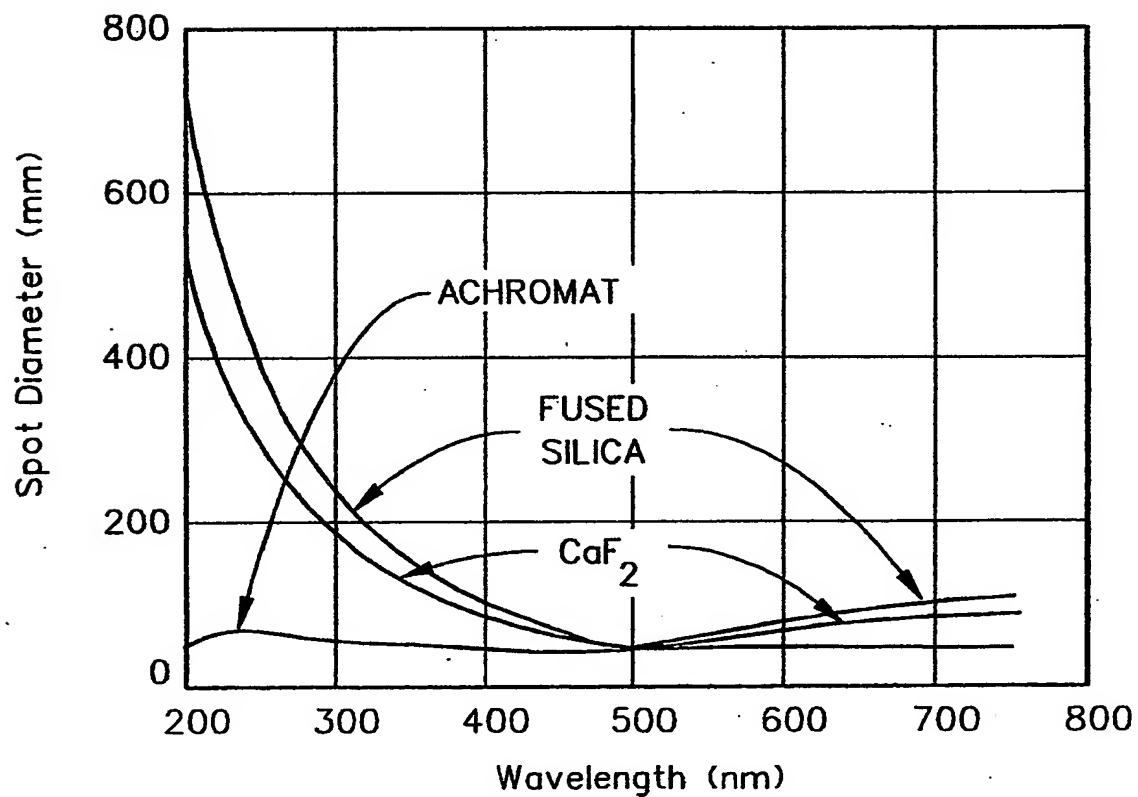


FIG. 7a2

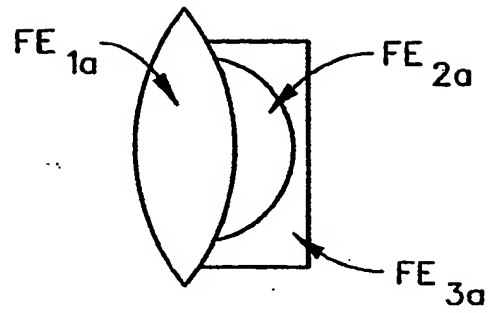


FIG. 7a<sub>3</sub>

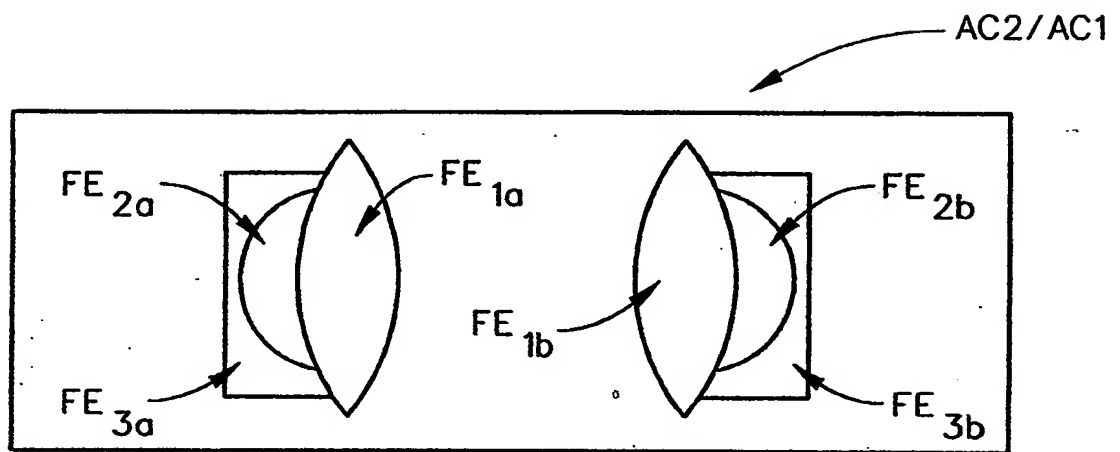


FIG. 7a<sub>4</sub>

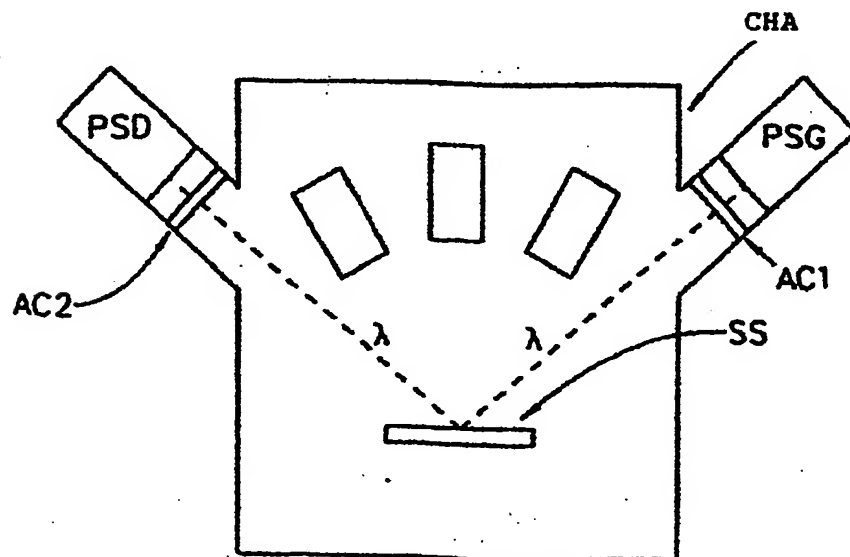


FIG. 8

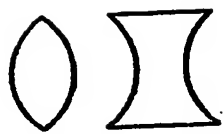


FIG. 7a5

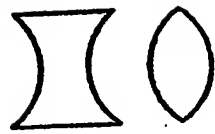


FIG. 7a6

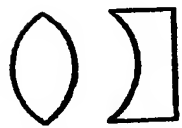


FIG. 7a7



FIG. 7a8

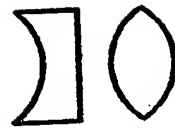


FIG. 7a9

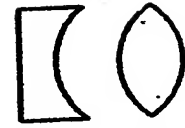


FIG. 7a10

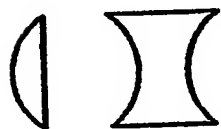


FIG. 7a11

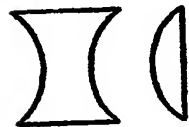


FIG. 7a12

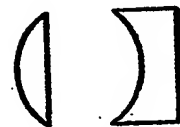


FIG. 7a13



FIG. 7a14



FIG. 7a15



FIG. 7a16

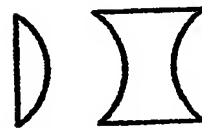


FIG. 7a17

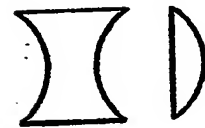


FIG. 7a18

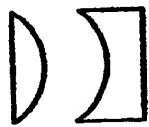


FIG. 7a19



FIG. 7a20



FIG. 7a21



FIG. 7a22

C D C D

FIG. 7a23

C D D C

FIG. 7a24

D C D C

FIG. 7a25

D C C D

FIG. 7a26

C C D

FIG. 7a27

C D C

FIG. 7a28

D D C

FIG. 7a29

D C D

FIG. 7a30

D C C

FIG. 7a31

C D D

FIG. 7a32